



17W 2133

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 09/932,086 Confirmation No.: 5457
Applicant(s) : Georg Farkas
Filing Date : 17 August 2001
Title : Arrangement for Testing Integrated Circuits
Group Art Unit : 2133
Examiner : Guy J. Lamarre
Docket No. : CH 000018

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
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CERTIFICATE OF MAILING (37 CFR §1.8)

I hereby certify, pursuant to 37 CFR §1.8, that this correspondence is being facsimile transmitted or deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below:

Dated: 28 July 2005

By:

Ariana Sanchez
Ariana Sanchez

AMENDMENT

SIR:

This paper is responsive to the non-final Office Action mailed May 12, 2005, having a shortened statutory response period expiring on August 12, 2005. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this document.

Remarks/Arguments begin on page 5 of this document.